

**Form PTO-1449** (modified)List of Patents and Publications  
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PATENT DOCKET NO: 5150-23006

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Technology Center 2100

SERIAL NO: 09/788,219

APPLICANT: Kodosky et al.

FILING DATE: February 16, 2001

GROUP: 2123

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

B1	Choosing Block-Diagram Tools for DSP Design, <a href="http://www.bdti.com/articles/info_dspmt95blockdiagram.htm">http://www.bdti.com/articles/info_dspmt95blockdiagram.htm</a> , May 9, 2003, pgs. 1-7.
B2	Real-Time-Workshop for Use with Simulink, User's Guide, May 1994, 229 pages.
B3	Guide to Rapid Prototyping with Simulink, Real-Time-Workshop and dSPACE, 1995, 16 pages.
B4	Real-Time-Interface to Simulink, RTI-30, User's Guide, 1995, 125 pages.
B5	Kevin J. Gorman and Kourosh J. Rahnamai, "Real-Time Data Acquisition and Controls Using MatLAB", 1995, 4 pages.
B6	SPW - MatLAB Co-Simulation Interface Product Data Sheet, 1996, 2 pages.
B7	Signal Processing WorkSystem, MatLAB Interface User's Guide, Oct. 1995, 72 pages.
B8	Alta Group of Cadence Design Systems, Inc., 1995, 34 pages.
B9	Code Generation System Product Data Sheet, 1994, 8 pages.
B10	SPW/CGS Porting Kits Product Data Sheet, 11/94, 2 pages.
B11	MultiProx for SPW Product Data Sheet, 08/94, 4 pages.
B12	DSP ProCoder for SPW Product Data Sheet, 11/94, 4 pages.
B13	Xanalog Corporation Sales Manual, January 1987, 8 pages.
B14	Available XA-1000 Literature and Its Use, 1986, 2 pages.
B15	Xanalog, XA-1000 Programming ICONS, 1986, 26 pages.
B16	Xanalog's CAE System: The Fastest AT Alive, Mass High-Tech, Vol. 4, No. 22, 08/1988, 1 page.
B17	Xanalog-The Computer-Aided Engineering Workstation Comes to Simulation, Simulation Vol. 47, No. 1, July 1986, 3 pages.
B18	Xanalog RT Real-Time Analog and Digital I/O, 10/90, 4 pages.
B19	Xanalog/SC+, 9/90, 4 pages.
B20	Xanalog Specializing in Workstations for Continuous Dynamic Simulation, 1987, 24 pages.

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**OTHER ART CONTINUED** (Including Author, Title, Date, Pertinent Pages, Etc.)

B21	Xanalog-Real-Time-User Guide, 1994, 28 pages.
B22	Lee et al., "Gabriel: A Design Environment for Programmable DSPs", 11/7/1988, 13 pages.
B23	Lee et al., "A Design Tool for Hardware and Software for Multiprocessor DSP Systems", May 1989, 4 pages.
B24	Gabriel 0.7 Overview, 1990, 5 pages.
B25	Joseph T. Buck and Edward A. Lee, "Scheduling Dynamic Dataflow Graphs with Bounded Memory Using the Token Flow Model", <a href="http://www.synopsys.com/">http://www.synopsys.com/</a> , 1995, 4 pages.
B26	Edward A. Lee, "Design Methodology for DSP", 1992, 4 pages.
B27	Pino et al., "Interface Synthesis in Heterogeneous System-Level DSP Design Tools", 05/1996, 4 pages.
B28	Jose Luis Pino, Master's Report, "Software Synthesis for Single-Processor DSP Systems Using Ptolemy", May 1993, 48 pages.
B29	Asawaree Kalavade and Edward A. Lee, "A Hardware-Software Codesign Methodology for DSP Applications", 1993, 12 pages.
B30	Pino et al., "Automatic Code Generation for Heterogeneous Multiprocessors", 1994, 4 pages.
B31	Tool Chest continues to Grow, Electronic Engineering Times, 12/15/1995, 2 pages.
B32	Pino et al., "Mapping Multiple-Independent-Synchronous dataflow Graphs onto Heterogeneous Multiprocessors", 10/1994, 6 pages.
B33	Asawaree Kalavade and Edward A. Lee, "Hardware/Software Co-Design Using Ptolemy - A Case Study", 09/1992, 18 pages.
B34	Pino et al., "Software Synthesis for DSP Using Ptolemy", 1995, 15 pages.
B35	Vol. 1 - Ptolemy 0.7 User's Manual, 1997, 532 pages.
B36	i-Logix Product Overview, 1996, 52 pages.
B37	Press Release, "i-Logix StateMate MAGNUM Supports PCs", 01/31/1997, 2 pages.
B38	Press Release, "i-Logix Signs Reseller Agreement for Virtual Prototypes, Inc.'s VAPS Product Line", 02/11/1997, 2 pages.
B39	Press Release, "i-Logix Introduces Rhapsody, Object-Oriented analysis, Design and Implementation Tool", 02/10/1997, 2 pages.
B40	StateMate/C Product Overview, 1995, 4 pages.
B41	Press Release, "i-Logix and Integrated Systems Link StateMate MAGNUM and MATRIX AutoCode", 01/03/1997, 2 pages.
B42	Press Release, "i-Logix and Wind River unveil Industry's First Rapid Prototyping Solution for Testing Embedded Systems at ESC West in San Jose, 09/17/1996, 3 pages.
B43	Press Release, "i-Logix Inc. Endorses Unified Modeling Language, 01/16/1997, 1 page.

XAMINER:

Russell Fred

DATE CONSIDERED:

24-DEC-03

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Information Disclosure Statement-PTO 1449 (modified)

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